

Compal Confidential

Schematics Document

QMLE4 LS-8865P USB & PWR_BTN /B

2011-04-05

REV:1.0

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/11/21	Deciphered Date	2013/11/21	Title	SCHEMATIC,USB/B S8865
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				Custom	405M2R
				Date:	Friday, July 06, 2012
				Sheet	1 of 3

COMPAL CONFIDENTIAL

MODEL NAME: QMLE4

PCB NAME: LS-8865P

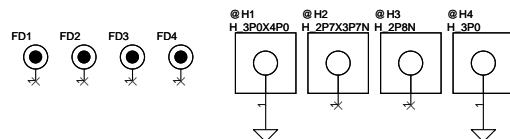
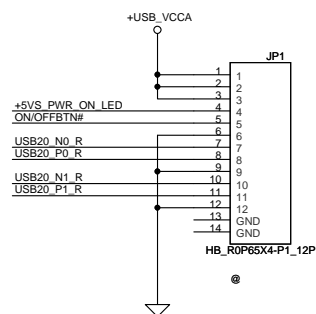
COMPAL P/N: DA40001C710

Function: USB & PWR_BTN

REVISION: 1.0

DATE: 2012/04/06

NOTICE: ON/OFFBTN# have pull-high resistance on motherboard



CSFFC1 14_45@



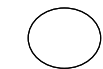
QMLE4_FFC

CSFFC2 15_45@



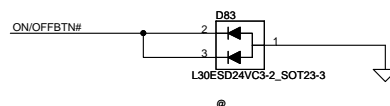
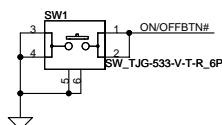
QMLE5_FFC

ZZZ



PCB LS8865P rev1

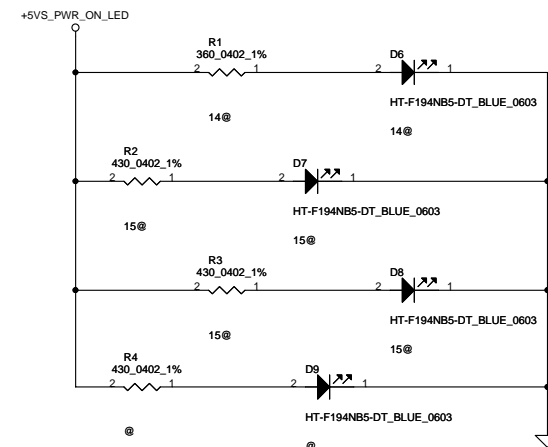
POWER BOTTON



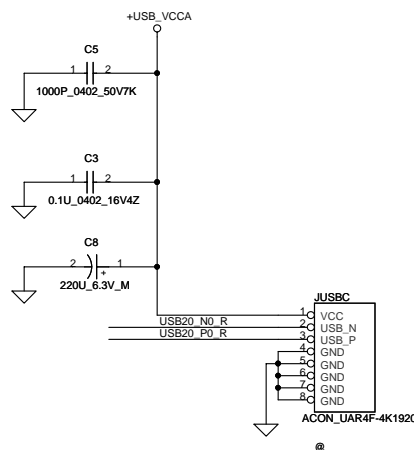
POWER LED

Vf= 2.55~3.15V If(Max)= 20mA

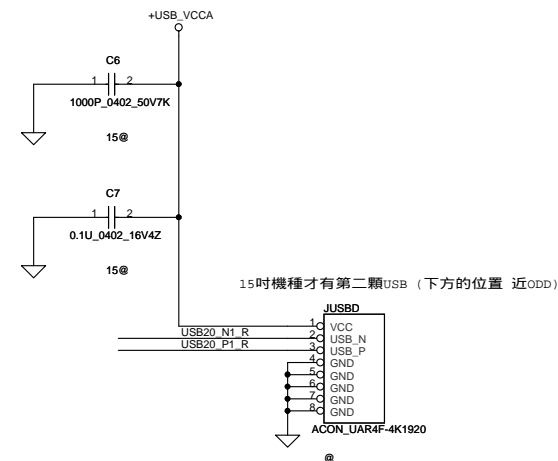
[NOTICE] LED resistance NOT on motherboard



USB CONNECTOR_0



USB CONNECTOR_1



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HW4 Product Improvement Record (P.I.R.)

XXXXE LS-XXXXP SCHEMATIC CHANGE LIST

REVISION CHANGE: 0.1 TO 0.1

GERBER-OUT DATE: 2011/11/21

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	2011-11-21	1,2,3	BUILD FIRST VERSION	
2	2011-11-23		Complete the initial version	
3	2011-12-01		ADD THREE ESD DIODE , ADD 4.7u	
4	2011-12-08	C8	Modify the big cap. SF000001500 to SF000001580	
5	2011-12-11		Fix the wrong connection and change the connector by ME information	
6	2011-12-13		Change the USB connector again and Modify the pin define. Delete 4.7u	
7	2012-02-09		add 4 LED(D6 D7 D8 D9) ,change D83	
8	2012-02-13		CHANGE 4 HOLE SIZE	
9	2012-04-06		CHANGE POWER BUTTON LED CURCUIT BY GIVE EACH LED A RESISTANCE	

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